

# Am335x Sitara Processors Ti

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### Am335x Sitara Processors Ti

#### **AM335x Sitara™ Processors - TI.com**

AM335x Sitara™ Processors 1 Device Overview 1 Texas Instruments Incorporated Peripherals [MMC and SD, UART, SPI, I2C], free of charge from TI  
The AM335x microprocessor contains the subsystems shown in the Functional Block Diagram and a brief description of each follows:

#### **AM335x Sitara Processors Silicon ... - Texas Instruments**

AM335x Sitara™ Processors Silicon Revisions 21, 20, 10 Silicon Errata SPRZ360I-October 2011-Revised January 2017 AM335x Sitara™ Processors Silicon Revisions 21, 20, 10 1 Introduction This document describes the known exceptions to the functional specifications for the AM335x Sitara™ ARM® Cortex®-A8 processors

#### **Sitara AM335x Power Solutions - Texas Instruments**

AM335x Power Solutions 2 Feature TPS650250 TPS65217 TPS65910 Power (DCDC/LDO) 3 DCDC / 3 LDO 3 DCDC / 2+2 LDOs 3 DCDC / 9 LDO  
Other features •Power-fail comparator •WLED driver •2LDO can be configured as load switches •I2C interface

#### **AM335x power options - Texas Instruments**

several OMAP, Sitara and other processors Several available ref-designs •Integration: 50% smaller footprint than discrete, ideal for portable applications Also 32KHz clock and RTC integration •Competitive pricing for cost-sensitive applications •Supported on AM335x EVM •2 step-down converters with Smart Reflex capability, for

#### **TI Sitara AM335x - Texas Instruments**

TI Confidential - NDA Restrictions TI Confidential - NDA Restrictions TI Sitara AM335x DDR3 Configuration & Software leveling 1 □□□

**SPRS717H -OCTOBER 2011-REVISED MAY 2015 AM335x ...**

AM335x Sitara™ Processors 1 Device Overview 11 Features 1 Texas Instruments Incorporated Submit Documentation Feedback Product Folder  
 Links: AM3359 AM3358 AM3357 AM3356 AM3354 AM3352 AM3359, AM3358, AM3357, AM3356, AM3354, AM3352 www.ti.com SPRS717H  
 -OCTOBER 2011-REVISED MAY 2015

**Texas Instruments EtherCAT on Sitara Processors**

Texas Instruments, Inc (TI) is the first semiconductor company to license Ether-CAT technology TI has integrated EtherCAT into several Sitara™ processors, including the AM335x ARM® Cortex-A8 and the AM437x ARM Cortex-A9 devices To enable EtherCAT, TI has built upon its programmable real-time unit (PRU) technology to create a

**EtherCAT® Master Stack for TI Sitara CPU Family**

www.ti.com TI Sitara Overview 3 TI Sitara Overview The Sitara AM335x processor is low-power device based on the ARM® Cortex®-A8 RISC This system on a chip (SoC) features a broad range of integrated peripherals that are well suitable for industrial applications Sitara processors support multiple operating frequency ranges from 300 MHz for simple

**EtherCAT® Master on Sitara™ Processors - TI.com**

This training will present a TI's ready-to-use EtherCAT masters solutions for Sitara processors EtherCAT master on Sitara runs on different operating systems, such as TI-RTOS and RT Linux, and runs in any of the devices EMACs interfaces \

**Introducing the AM57x Sitara Processors from Texas Instruments**

Introducing the AM57x Sitara Processors from Texas Instruments ARM Cortex-A15 solutions for automation, HMI, vision, analytics, and other industrial and high-performance applications Embedded Processing Sitara ARM Processors

**AM335x Starter Kit Quick Start Guide (Rev. B) - TI**

22 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI,

**Sitara™ AM335x ARM Cortex™-A8 Microprocessors (MPUs ...**

This document describes the known exceptions to the functional specifications for the Sitara™ AM335x ARM® Cortex™-A8 Microprocessors (MPUs) [See the Sitara AM335x ARM Cortex-A8 Microprocessors (MPUs) data manual (literature number SPRS717)] For additional information, see the latest version of the Sitara AM335x ARM Cortex-A8 Microprocessors

**Sitara ARM Processors Brochure - esys.ir**

Bringing together flexible hardware solutions with robust software tools enables you to use Sitara processors to create inspiring ARM designs that start to bridge the gap between high-end MCUs and mid-range ARM processor-based devices Highly optimized solutions for the embedded market Sitara AM335x ARM Cortex-A8 processors deliver right

**MYC-AM335X CPU Module - MYIR Tech**

Measuring only mm by 70 50mm, the MYC-AM335X is a highly-integrated low-cost ARM embedded SOM (System on Module) supporting up to 1GHz Sitara AM335x (AM3352, AM3354, AM3356, AM3357, AM3358 and AM3359) ARM Cortex-A8 processors from Texas Instruments (TI), featuring PowerVR™ SGX530 for 2D and 3D graphics

**AM335x Sitara™ Processors**

AM335x Sitara™ Processors 1 Device Overview 1 Texas Instruments Incorporated • Five ADPLLs to Generate System Clocks (MPU Subsystem, DDR

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Interface, USB and from TI The AM335x microprocessor contain the subsystems shown in Figure 1-1 and a brief description of each follows:

### **Sitara Linux Training: Hands on with the SDK**

Sitara Linux Training: Hands on with the SDK 2 Out-Of-Box Experience Description This section will cover the steps to boot the target board and how to use the Matrix application launcher, which is the

### **Parallel Camera Interface for Sitara Processors ... - TI**

Parallel Camera Interface for Sitara Processors Design Guide TI Designs TI Designs provide the foundation, including methodology, testing, and design file, for quickly your time to market Design Description This camera interface design connects to a 10-bit parallel interface to the AM335x general purpose memory controller (GPMC) 16

### **Punya Prakash, Kazunobu Shin, Systems Engineer, Power ...**

Texas Instruments 5 TI's Sitara AM335x processors support five DPLLs, which in turn can support independent frequency control, regulate processor, DDR and display clock frequency based on the application needs In addition to this, six dual-voltage I/O banks can be configured for 18V or 33V The 18V I/O will benefit reduction of processor

### **Workshop Setup Guide - software-dl.ti.com**

You should not select "TI ARM Compiler" This compiler does not have Linux support and is used when Sitara processors are run without the Linux operating system